Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L4	20509	(die or (integrated adj circuit) or IC) same differential	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/22 08:49
L5	109295	(die or (integrated adj circuit) or IC) same (carrier or package)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/22 07:54
L6	2965	4 and 5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/22 07:54
L7	1282	width and 6	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/22 07:54
L8	135	(impedance same width) and 7	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/22 07:55
L9	83	(impedance with width) and 7	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/22 07:56
L10	3	escap$3 and 9	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/22 08:05
L11	62	transmission and 9	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/22 08:51
L12	19166	chip near2 carrier	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/22 08:30
L13	7	(chip near2 carrier) same ((constant or uniform or identical) near3 impedance)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/22 08:49
L14	1	differential and 13	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/22 08:49
L15	105	(chip near2 carrier) and ((constant or uniform or identical) near3 impedance)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/22 08:49

L16	18	differential and 15	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/22 08:49
L17	16	transmission and 16	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/22 08:51
L22	2485	716/1,5.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/22 15:17
L23	1095	716/2.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/22 15:04
L25	3062	333/32-34.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/22 15:18
S32	4	(("4593243") or ("5138287")).PN. or (2002/0079983).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/09/21 16:09
S33	2	"6677831"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/21 16:10
S34	6	S32 or S33	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/21 16:10
S36	2	(die or (integrated adj circuit) or IC) and S34	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/22 07:50
S37	6803	(die or (integrated adj circuit) or IC) and ((printed adj circuit) or PCB) and ((chip adj carrier) or (package near2 pin))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/21 16:20
S38	887	differential and S37	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/21 16:21
S39	9	((constant or uniform) near3 impedance) and S38	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/21 16:35

S40	9	((constant or uniform or identical) near3 impedance) and S38	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/21 16:35